

2818

FEB 20 2003

Docket No.: WMP-EUP-008

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

By: Markus Nolf

Date: February 12, 2003

#8 election

M. Braun

2/26/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Gottfried Ferber et al.  
 Applic. No. : 10/056,770  
 Filed : January 24, 2002  
 Title : Semiconductor Module and Method for Fabricating the Semiconductor Module  
 Examiner : Long K. Tran - Art Unit: 2818

RESPONSE

Hon. Commissioner of Patents and Trademarks,  
 Washington, D. C. 20231

Sir:

Responsive to the Office Action dated January 16, 2003, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-11, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-11 is solicited.

Respectfully submitted,

Markus Nolf

MARKUS NOLFF  
 REG. NO. 37,006

For Applicants

Date: February 12, 2003

Lerner and Greenberg, P.A.  
 Post Office Box 2480  
 Hollywood, FL 33022-2480  
 Tel: (954) 925-1100  
 Fax: (954) 925-1101  
 /bmb

RECEIVED  
 FEB 25 2003  
 TECHNOLOGY CENTER 2800